

## **MEMORY MAPPED REGISTER FILE**

### **RELATED APPLICATIONS**

[001] This application claims priority to U.S. Provisional Patent Application No. 60/468,802, entitled "MEMORY MAPPED REGISTER FILE," filed May 7, 2003, which is expressly incorporated herein by reference. This application is also related to commonly owned U.S. Patent Application No. 10/672,774, entitled "DATA PROCESSING SYSTEM WITH REORDER BUFFER AND COMBINED LOAD STORE ARITHMETIC LOGIC UNIT AND PROCESSING METHOD THEREOF," filed on 09/26/2003, which is expressly incorporated herein by reference.

### **FIELD**

[002] This invention relates generally to data processing systems and, more particularly, to a memory mapped register file for a data processing system.

### **BACKGROUND**

[003] General purpose registers or a "register file" are an essential component of a data processing system's processing architecture. For instance, a microprocessor or central processing unit (CPU) of a data processing system retrieves and stores data from and to one or more general purpose registers to process instructions. These registers allow the data processing system to perform instructions more efficiently. Many prior microprocessor architectures use sixteen general purpose registers designated, e.g., R0 through R15, and operate in different processor modes.

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
www.finnegan.com